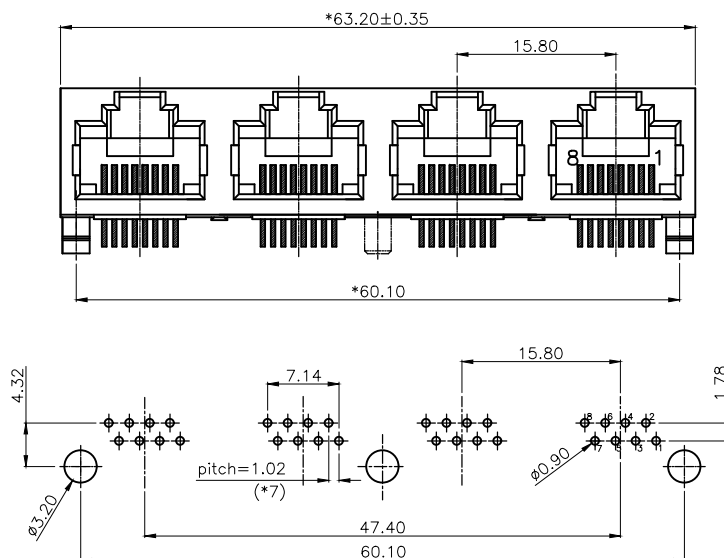
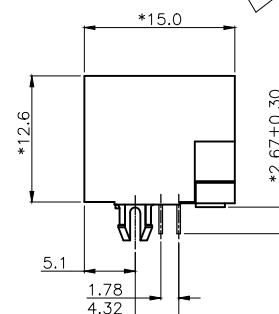


REV	ECO	DESCRIPTION	DATE	DRFT	CHKD



PC Board Layout(t=1.60)
Component Side Shown



NOTES:

ELECTRICAL:

1. VOLTAGE RATING : 125 VAC RMS.
2. CURRENT RATING : 1.5 AMP.
3. CONTACT RESISTANCE : 30 MILLIOHMS MAX.
4. INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
5. DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.
6. LED RATINGS: FORWARD VOLTAGE: 2.2 VOLTS TYPICAL.
REVERSE VOLTAGE: 5 VOLTS MAXIMUM.
7. OPERATING: 750 CYCLES MIN.

MECHANICAL:

1. HOUSING MATERIAL : PBT UL94V-0.
2. INSERT MATERIAL : PBT UL94V-0.
3. BACK PLATE MATERIAL: PBT UL94V-0.
4. LED SIZE : 3.2*2.0*7.0*P=2.54mm.
5. CONTACT MATERIAL : PHOSPHOR BRONZE t=0.35mm OR ϕ 0.46mm.
6. PLATING : GOLD PLATING OVER NICKEL.
7. PCB RETENTION PRE-SOLDER: 1 LB MIN.
8. PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

1. STORAGE : -40°C TO +85°C.
2. OPERATION: -40°C TO +85°C.
3. WAVE SOLDERING TEMPERATURE: 230°C ~250°C (3~5 SECONDS).
4. RoHS COMPLIANT.
- MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.
CUL FILE NO. E345295

PART NUMBER: EL2J88-X00XXX

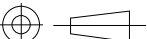
0-W/O LOGO-

4-4 PORTS W/O MIDDLE POLE
C-4 PORTS W/ MIDDLE POLE

—1-3u" 2-6u" 3-15u"
4-30u" 5-50u" F-GOLD FLASH

—G—STAMPED PIN.
H—ROUND PIN

—O—PBT BLACK
M—PBT PANTONE YELLOW 114C

單位: mm	DFTO: Ina	DATE: 2017.11.09	ELLAX CORPORATION TITLE: LED JACK UTP W/O LED(W/O SHIELD)		
長度公差 角度公差	CHKD: Allen	DATE: 2017.11.09			
.0±0.25 0°±5°	APPVL: W.B.Huang	DATE: 2017.11.09			
.00±0.15	脫模角:	顏色:			
.000±0.10		MATERIAL: 材 質	DWG NO. EL2J88-00040HF SIZE REV DRAWING NO: EL2J88-00040HF_A A4 A SCALE: 2.5 : 1 PART NO: 零件編號 1 of 1		
		TREAT: 處 理			
		QTY: 數 量			
THIRD ANGLE PROJECTION		SCALE: 2.5 : 1			